



Model Number : OPC-X128U-X0

OLED Control Board DATASHEET

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CONFIDENTIAL

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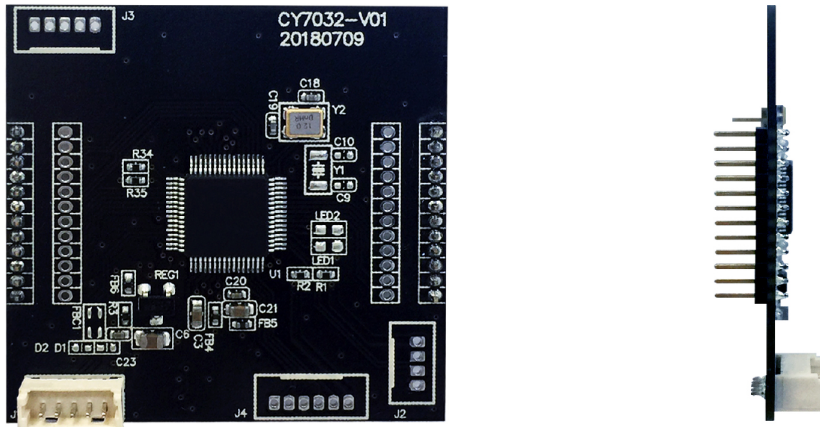
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1. FEATURES

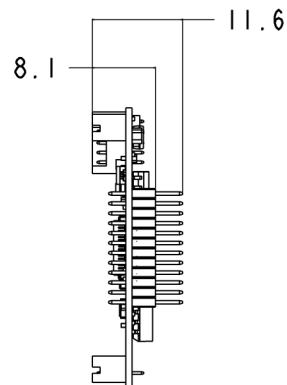
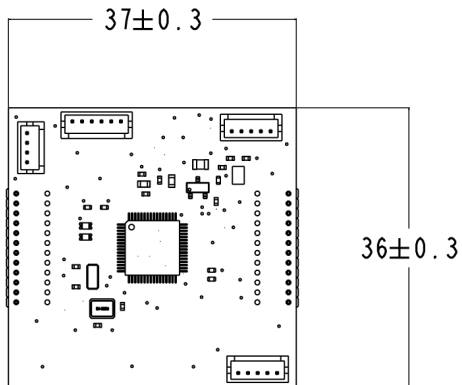
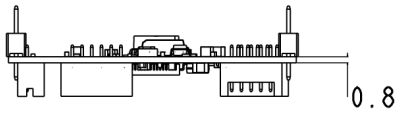
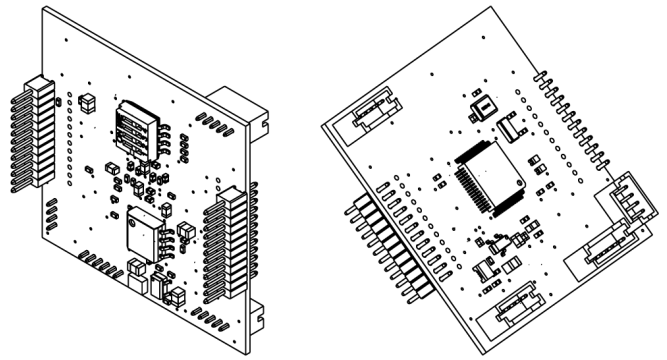
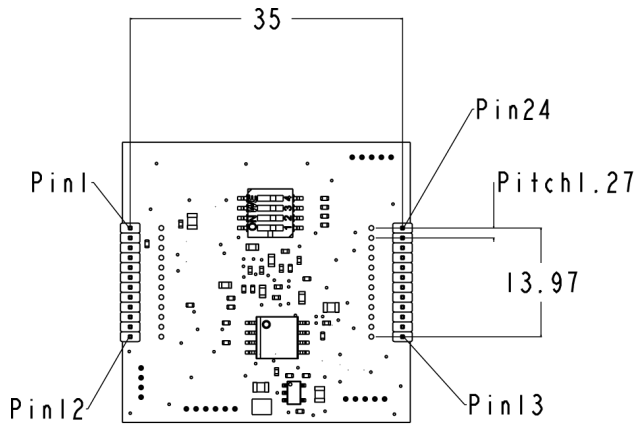


- Corresponding Module: ODC-X128U-X0
- Interface: USB Interface
- Software: SDK with API (Windows and Linux)

2. GENERAL SPECIFICATION

ITEM	DIMENSION	UNIT
Display Type	OLED	
Corresponding Module	ODC-X128U-X0	
Module Dimension	37.0 x 36.0 x 11.6	mm
Assembly	12 Pin Connector x 2 / Pitch 1.27	
Supply Voltage	5V	
DC	Built in DC-DC Power Supply to Drive OLED	
Interface	USB	

3. MECHANICAL DRAWING



***Model: OPC-X128U-X0

Dimensions in millimeters

4. INTERFACE PIN FUNCTION

PIN NO.	SYMBOL	TYPE	FUNCTION															
1	VDD	P	Power Supply for Core VDD This is a voltage supply pin. It must be connected to external source.															
2	VSS		Ground for System This is a ground pin. It must be connected to external source.															
5 6	BS1 BS2	I	Communicating Protocol Select These pins are MCU interface selection input. See the following table: <table border="1" style="margin-left: 20px;"> <thead> <tr> <th></th> <th>BS1</th> <th>BS2</th> </tr> </thead> <tbody> <tr> <td>3-wire SPI</td> <td>1</td> <td>0</td> </tr> <tr> <td>4-wire SPI</td> <td>0</td> <td>0</td> </tr> <tr> <td>68xx-parallel (8bit)</td> <td>1</td> <td>1</td> </tr> <tr> <td>80xx-parallel (8bit)</td> <td>0</td> <td>1</td> </tr> </tbody> </table>		BS1	BS2	3-wire SPI	1	0	4-wire SPI	0	0	68xx-parallel (8bit)	1	1	80xx-parallel (8bit)	0	1
	BS1		BS2															
3-wire SPI	1		0															
4-wire SPI	0		0															
68xx-parallel (8bit)	1		1															
80xx-parallel (8bit)	0		1															
7	CS#		Chip Select This is the chip select input. The chip is enabled for MCU communication only when CS# is pulled low.															
8	RES#		Power Reset for Controller and Drive This is reset signal input. When the pin is low, initialization of the chip is executed.															
9	D/C#		Data/ Command Control This pin is Data/Command control pin. When the pin is pulled high, the input at D0~D7 is treated as display data. When the pin is pulled low, the input at D0~D7 will be transferred to the command register.															
10	WR# (R/W#)		Write or Read/Write Select When 80xx interface mode is selected, the pin will be the Write (WR#) input. When interfacing to a 68xx-series microprocessor, the pin will be used as Read/Write (R/W#) selection input. Pull this pin to "High" for read mode and pull it to "Low" for write mode.															
11	RD#(E)		Read or Read/Write Enable When 80xx interface mode is selected, the pin will be the Read (RD#) input. When interfacing to a 68xx-series microprocessor, the pin will be used as the Enable (E) signal. Read/Write operation is initiated when this pin is pulled high and the CS# is pulled low.															
13-20	D0-D7	Host Data Input /Output Bus These pins are 8-bit bi-directional data bus to be connected to the microprocessor's data bus. When serial mode is selected, D1 will be the serial data input SDIN and D0 will be the serial clock input SCLK. Unused pins must be connected to VSS except for D2.																
21	VSS	Ground for System This is a ground pin. It must be connected to external source.																
22	VCC-CTL	OLED Driver Power Supply ON/ OFF Control When this pin is pulled high, the panel power supply will be turned ON. When this pin is pulled low, the panel power supply will be turned OFF.																
24	VCC	OLED Driver Power Supply Output This pin is OLED driver power supply output. When VCC-CTL is pulled high, the pin will be output about 13V voltage.																
3, 4, 12 & 23	NC	-	Reserved Pin															